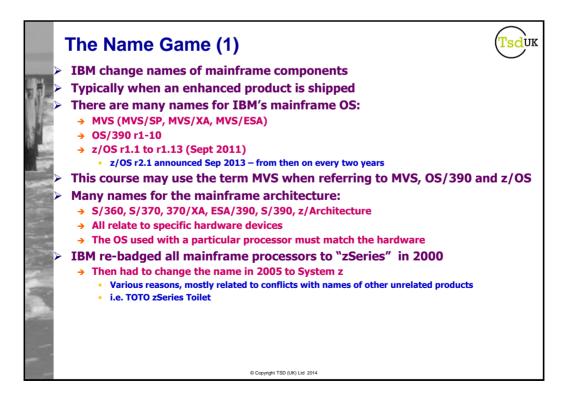
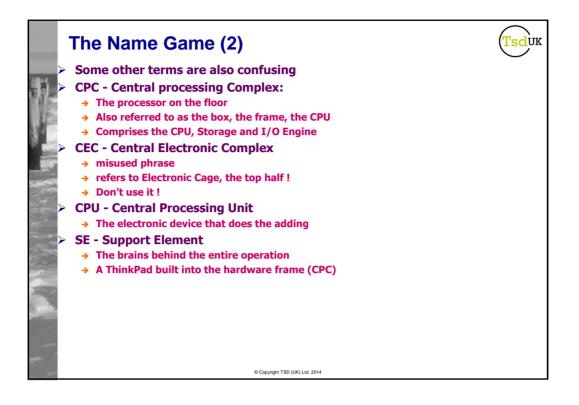
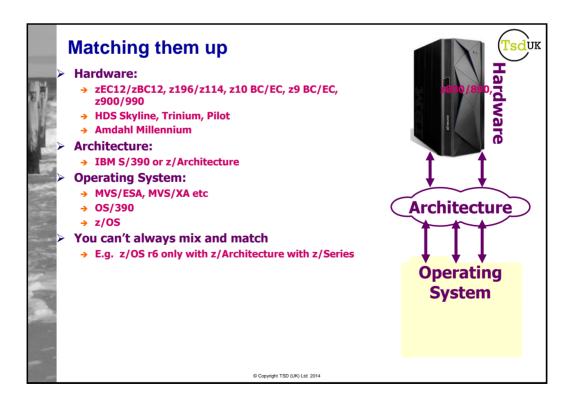
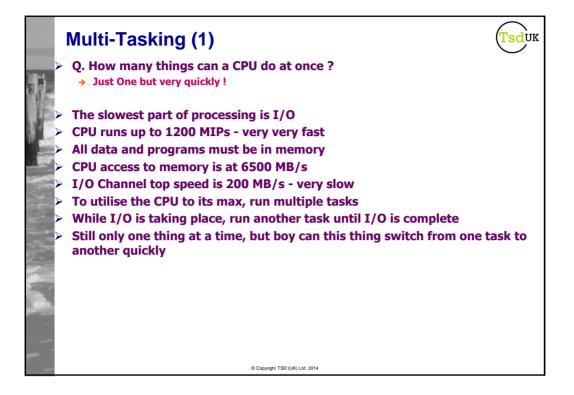


Example of POP - MVC	Tsduk
MVI D1(B1),12 [SI]	
1 12 + 12 + B1 + D1	
0 8 16 20 31 MVC D1(L,B1),D2(B2) [SS]	
2 1 'D2' L B1 D1 B2 D2 0 8 16 20 32 36 47	
The second operand is placed at the first-operand location.	
For MOVE (MVC), each operand is processed left to right. When the operands overlap, the result is obtained as if the operands were processed one byte at a time and each result byte were stored immediately after fetching the necessary operand byte.	
For MOVE (MVI), the first operand is one byte in length, and only one byte is stored.	
Condition Code: The code remains unchanged.	
Access (fetch, operand 2 of MVC; store, operand 1, MVI and MVC)	
Programming Notes: 1. Examples of the use of the MOVE instruction are given in Appendix A, "Number Representation and Instruction-Use Examples." 2. It is possible to propagate one byte through an entire field by having the first operand start one byte to the right of the second operand.	
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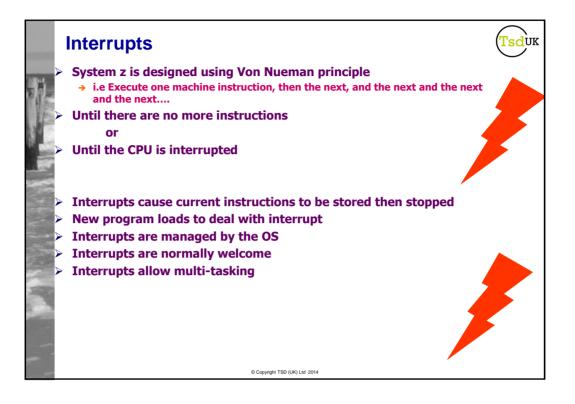


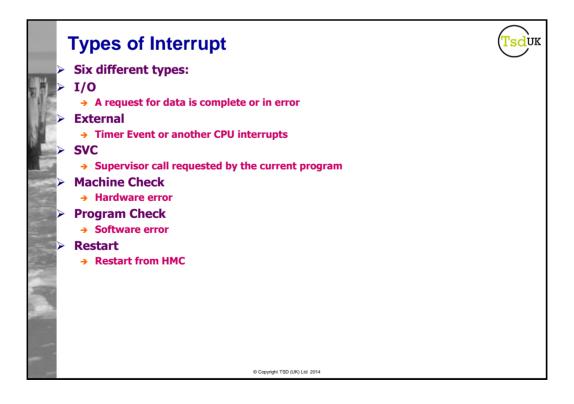


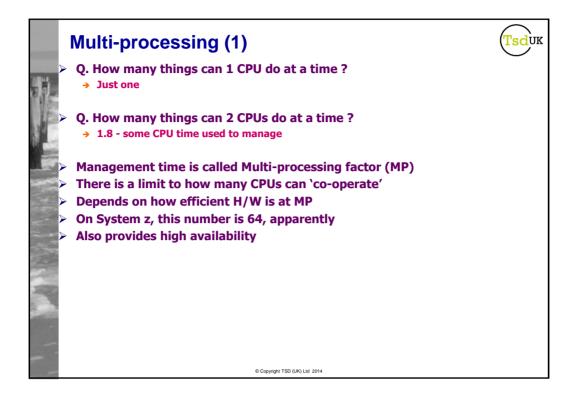




	Multi-tasking (2) Needs an operating system to manage and supervise this process Each task needs its own storage to exist in OS provides for Multiple 'chunks' of Virtual Storage, hence the name MVS Each 'chunk' is called an Address Space						
	Prog A running	Prog B waiting for I/O	Prog C waiting for I/O	Prog D waiting for CPU	Prog E waiting for CPU		
1.1.1			Z/OS CPU © Copyright TSD (UK) Ltd 2014				







-11	Multi-proce	essing (2)				Ts	duk
	Prog A Prog E running waitin on CPU1 for I/0		g	Prog C waiting for CPU	iting running		Prog E running on CPU3	
L'IL	z/OS							
	CPU 1			CPU 2		CPU 3		
11				© Copyright TSD (UK) Ltd 2014				

